

FORM PT01449
(REV. 8-83)U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE

ATTY. DOCKET NO.

HARI-0606

SERIAL NO.

08/174,768

INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

APPLICANT

Eliyahou Harari

FILED

December 29, 1993

GROUP

2413

U. S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE
<i>LOV</i>	A17	4 0 9 3 9 8 5	6/1978	Das	395	185.02x	
<i>LOV</i>	A18	4 2 7 9 0 2 4	7/1981	Schrenk	365	185.22	
<i>LOV</i>	A19	4 6 1 6 3 1 1	10/1986	Sato	395	416	
<i>LOV</i>	A20	4 7 1 8 0 4 1	1/1988	Baglee et al.	365	185.22	
<i>LOV</i>	A21	4 7 8 5 4 2 5	11/1988	Lavelle	365	52	
<i>LOV</i>	A22	4 8 0 0 5 2 0	1/1989	Iijima	235	382	
<i>LOV</i>	A23	4 8 8 7 2 3 4	12/1989	Iijima	395	497.04	
<i>LOV</i>	A24	4 9 4 9 2 4 0	8/1990	Iijima	395	600	
<i>LOV</i>	A25	5 0 5 3 9 9 0	10/1991	Kreifels et al.	395	430	
<i>LOV</i>	A26	5 0 7 0 4 7 4	12/1991	Tuma et al.	395	416	
<i>LOV</i>	A27	5 0 9 5 3 4 4	3/1992	Harari	365	185.03x	

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANS.? (YES/NO)
<i>LOV</i>	B10	58 - 2 1 5 7 9 4	12/1983	Japan	—	—	YES
<i>LOV</i>	B11	58 - 2 1 5 7 9 5	12/1983	Japan	—	—	YES
<i>LOV</i>	B12	59 - 1 6 2 6 9 5	9/1984	Japan	—	—	YES
<i>LOV</i>	B13	60 - 2 1 2 9 0 0	10/1985	Japan	—	—	YES
<i>LOV</i>	B14	62 - 2 8 3 4 9 6	12/1987	Japan	—	—	YES
<i>LOV</i>	B15	60 - 0 7 6 0 9 7	4/1985	Japan	—	—	Abstr

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent pages, Etc.)

<i>LOV</i>	C5	Lucero et al., "A 16 kbit Smart 5 V-only EEPROM with Redundancy," IEEE Journal of Solid-State Circuits, vol. SC-18, no. 5, pps. 539-543 (October 1983)
<i>LOV</i>	C6	Torelli et al., "An improved method for programming a word-eras-able EEPROM," Alta Frequenza, vol. 52, no. 6, pps. 487-494 (Nov.-Dec. 1983)

EXAMINER

Ly V. Hua

DATE CONSIDERED

8/16/96

* EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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<i>WV</i>	B16 59 - 4 5 6 9 5	3/1984	Japan	—	—	Abstr.
<i>WV</i>	B19 61 - 9 6 5 9 8	5/1986	Japan	—	—	Abstr.
<i>WV</i>	B23 62 - 2 8 3 4 9 7	12/1987	Japan	—	—	Abstr.
<i>WV</i>	B24 63 - 1 8 3 7 0 0	7/1988	Japan	—	—	Abstr.

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent pages, Etc.)

<i>WV</i>	C7	Data Sheet: "27F256 256K(32K x 8) CMOS Flash Memory," Intel Corporation, pps. 1-24 (May 1988)
<i>WV</i>	C8	Preliminary Data Sheet, "48F512 512K Flash EEPROM," SEEQ Technology, Incorporated, pps. 2-1 thru 2-12 (October 1988)
<i>WV</i>	C9	Advanced Data Sheet, "48F010 1024K Flash EEPROM," SEEQ Technology, Incorporated, pps. 2-13 thru 2-24 (October 1988)
<i>WV</i>	C10	Lai, Robert S., Writing MS-DOS Device Drivers, The Waite Group, Inc., September 1987, pp. i-xi and 235-319

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